Application No.	Applicant(s)	
10/605,646	505,646 TU ET AL.	
Examiner	Art Unit	
Hashem Farrokh	2187	
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6. Interview Sur Paper No./M 8), 7. Examiner's A 8. Examiner's S	mmary (PTO-413),  Mail Date Amendment/Comment  Statement of Reasons for Allow	
	10/605,646  Examiner  Hashem Farrokh  Pars on the cover sheet with (OR REMAINS) CLOSED in or other appropriate communication is suit and MPEP 1308.  Index 35 U.S.C. § 119(a)-(d) or been received.  Index been received in Application cuments have been received of this communication to file alternation.  Itted. Note the attached EXAI as reason(s) why the oath or in the best been received.  In the submitted.  In the submitted of BIOLOGICAL MATE FOR THE DEPOSIT OF BIOLOGICAL MATE FOR THE METER FOR THE METE	10/605,646   TU ET AL.     Examiner

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#### **EXAMINER'S AMENMENT**

The Examiner initiated a telephone call to the Applicant (Mr. Winston Hsu 41,526) on 12/21/05. The examiner informed the Applicants that the Application is now in condition for allowance. The Examiner inquired whether the Applicant would be willing to cancel claim 6 which has a withdrawn status. The Applicant authorized the examiner to cancel claim 6. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Please amend claim 6 as follows:

### Claim 6:

Please cancel claim 6.

# PART I THE EXAMINER'S EVALUATION OF THE APPLICATION SECTION A RELAVANT ART CITED BY THE EXAMINER

- 1. U. S. Patent No. 5,481,689 to Stamm et al. describes Conversion of internal processor register commands to I/O space addresses.
- 2. U. S. Patent No. 5,038,280 to Watanabe et al. describes Information processing apparatus having address expansion function.
- 3. U. S. Patent No. 4,399,507 to Cosgrove et al. describes Instruction address stack in the data memory of an instruction-pipelined processor.

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4. U. S. Patent No. 5,255,382 to Pawloski describes Program memory expander for 8051-based microcontrolled system.

## SECTION B <u>DISTINGUISHING FEATURES RECITED IN THE CLAIMS</u>

The following is an **Examiner's Statement of Reasons for Allowance** See MPEP 1302.14

5. The primary reasons for allowance of independent claims 1-5 and 7-17 in the instant application is the combination with the inclusion of the following limitations:

wherein the conventional internal memory and the extended internal memory are in a same block of memory; use the extended internal memory as a stack

The prior art of record including the disclosures of Stamm et al. (5,481,689), Watanabe et al. (5,038,280), Cosgrove et al. (4,399,507), and Pawloski (5,255,382) neither anticipates nor renders obvious the above-recited combination.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays should be clearly labeled "Comments on Statement of Reasons for Allowance"

## PART II THE ATTENTION OF FUTURE CORRESPONDENT

Any inquiry concerning this communication should be directed to Hashem Farrokh whose telephone number is (571) 272-4193. The examiner can normally be reached Monday-Friday from 8:00 AM to 5:00 PM.

### :IMPORTANT NOTE:

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If attempt to reach the above noted Examiner by telephone are unsuccessful, the examiner's supervisor, Mr. Donald A Sparks, can be reached on (571) 272-4201. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published application may be obtained from either private PAIR or Public PAIR. Status information for unpublished application is available through Private PAIR only. For more information about PAIR system, see <a href="http://pair-direct.uspto.gov">http://pair-direct.uspto.gov</a>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBS) at 866-217-9197 (toll-free).

2005-12-16

SUPERVISORY PATENT EXAMINER